

A Direct Matrix Inversion-Less Analysis for Distribution System Power Flow Considering Distributed Generation

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ABSTRACT: This paper presents a new direct matrix inversion-less analysis for radial distribution systems (RDSs). The method can successfully deal with weakly meshed distribution systems. (WMDSs). Being easy to implement, direct methods (DMs) provide an excellent performance. Matrix inversion is the main reason of divergence and low-efficiency in power flow algorithms. In this paper, the performance of the proposed DM is upgraded since it is no longer necessary to use the matrix inversion and its related computations. On the other hand, DMs have not been explicitly developed for different models of loads and, moreover, analyzing meshes in previous works decreases the efficiency of such methods. This paper deals with these issues and takes a few steps forward. The modeling of distributed generations in distribution systems is also studied which is an interesting topic nowadays. Simulations results for balanced, unbalanced, meshed and radial systems with distributed generations verify the performance of the proposed method.

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1- Introduction

Almost in all topics relevant to expansion, operation, and management of DSs, it is very important to solve the power flow problem as efficient and simple as possible. The traditional power flow algorithms are not suitable for DSs. Therefore, several algorithms for solving distribution power flow problem have been proposed. These algorithms based on basic approaches which have been used to construct them can be classified into three categories: (a) methods based on Newton-Raphson (NR) and Newton-like methods, (b) backward/forward sweep methods (BFSMs) and (c) DMs. To modify conventional power flow methods, NR-based methods have initially been suggested to solve ill-conditioned power systems [1]-[3]. Then, methods based on general meshed topology such as transmission systems have been proposed [4]-[8]. A branch-current based NR approach has been proposed in [9]. Fast decoupled Newton methods for DSs have been developed in [10]-[12]. In recent years, current injection methods (CIMs) based on current injection equations have been proposed [13]-[15]. Generally speaking, these methods lack the advantages of simple implementation and low computations since they have not explicitly exploited characteristics of DSs.

BFSMs based on network topology of DSs are very prevalent due to their promising performance and simplicity in implementation [16]-[23]. The main idea of these methods has been suggested in [16] in which a new power flow method for the calculation of branch current flows has been proposed using compensation based technique and Kirchhoff's laws. Future developments of BFSMs for real-time analysis with an

emphasis on modeling of unbalanced and distributed loads in [19], and the extension version allowing modeling of voltage-dependent loads in [20] have been presented. In [24], the newest BFSM is presented and it is claimed that this method is faster and more flexible than the other BFSMs. One of the main disadvantages of BFSMs is poor convergence for WMDSs [25].

The third category was based on formation of an impedance matrix and solving equations in the form of $V=ZI$ [26]-[29]. The method which can express the voltage of each load in terms of load's currents and common impedances of the loads for RDSs has been proposed in [26]. In [27]-[28], the author develops two matrices to express bus voltages as a function of branch currents, line parameters, and the substation bus voltage. In recent years, two new DMs have been presented [30]-[31] but matrix inversion is needed by such methods.

The DMs as will be shown in the simulation results have a better performance over the two other methods. However, they have not been fully developed. They only solve meshes at the end of feeders and, moreover, it is not clear how they deal with different models of loads. Moreover, in some papers, DMs use additional features to solve meshes which decrease their efficiency.

This paper presents a new direct power flow method trying to deal with the above issues. The main advantages of the proposed method are I) its robustness and efficiency upgraded considerably since it is no longer necessary to use matrix inversion- being the main reason of divergence and low-efficiency of the power flow algorithms, b) its capability to solve meshes as efficiently and simply as RDSs where only a current ratio is used, c) its capability to solve DSs, even including more than one mesh.

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can deal with different models of loads and meshes. However, they usually use NR-based methods to solve DSs because of shortcomings of BFSMs and DMs. Therefore, they lack advantages of DMs such as simplicity of implementation, low computations, and robustness.

The present paper is organized as it follows. First, the proposed method is developed for RDSs in Section 2 where different models of loads are also studied. Modification of the proposed method for WMDSs is presented at Section 3. DG Modeling restricted to PQ mode is studied in Section 4. In the end, test results are presented in Section 5.

2- Algorithm Development for RDSs

The radial characteristic of DSs can be used to solve them as simple circuits. Consider a DS, including only a radial main feeder as shown in Figure 1. If the ladder iterative technique suggested by [30] is used for this feeder, after some manipulation, the voltage of load i at k -th iteration can be obtained by

$$V_i^k = V_{i-1}^k - z_i(I_i^{k-1} + I_{i+1}^{k-1} + \dots + I_n^{k-1}) \quad (1)$$

where z_i is impedance of the branch located between load $i-1$ and load i , and I_i^{k-1} is the current injection of bus i at $(k-1)$ -th iteration.

The voltage of bus 1 with respect to the substation bus voltage (V_s) and current injections can be expressed as:

$$V_1^k = V_s - z_1(I_1^{k-1} + I_2^{k-1} + \dots + I_n^{k-1}) \quad (2)$$

Repeating the same process for other buses yields the following formula as

$$V_i^k = V_s - \sum_{r=1}^i \sum_{j=r}^n z_r I_j^{k-1} \quad (3)$$

The unknown values in (3) can be restricted to current injections if V_i is expressed in terms of I_i and the equivalent load impedance of bus i . To apply such simplification and obtain the equivalent load impedance, the model of loads connected to bus i must be considered as [32]:

a) Constant power loads

for such loads, V_i can be written as:

$$V_i = Z_{P_i} I_{P_i} = \left(|V_i|^2 / S_{P_i}^* \right) I_{P_i}, \quad (4)$$

where Z_{P_i} , I_{P_i} , and S_{P_i} are the equivalent impedance, current and power of the constant power load connected to bus i , respectively.

b) Constant impedance loads

the impedance of such loads is constant, so V_i can be written as

$$V_i = Z_{Z_i} I_{Z_i}, \quad (5)$$

where Z_{Z_i} and I_{Z_i} are the impedance and the current of the constant impedance load connected to bus i , respectively.

c) Constant current loads

in this model, the magnitude of load current and also the current injection of bus i are constant. Therefore, the number of unknown values (current injections) is decreased; hence, the equation of V_i can be neglected. However, the phase of the current of such load changes to keep the power factor constant, it must be updated at each iteration as

$$I_{C_i} = |I_{C_i}| \angle (\delta_i - \theta_i) \quad (6)$$

where I_{C_i} and θ_i are the current and power factor angle of the constant current load connected to bus i , respectively. Note that δ_i denotes the phase of V_i .

d) Combination loads

a combination load can be modeled by assigning a percentage of the total load to each of the three mentioned models. The total line current entering the load is the sum of the three components as shown in Figure 2 [32]. In this condition, the equivalent load's impedance, current, and voltage of bus i can be calculated as

$$Z_{L_i} = Z_{P_i} || Z_{Z_i} \quad (8)$$

$$I_i = I_{P_i} + I_{Z_i} + I_{C_i} \quad (9)$$

$$V_i = Z_{L_i} (I_i - I_{C_i}) \quad (9)$$

Consequently, in general case, equation (3) after some manipulation can be rewritten as:

$$Z_{L_i}^k (I_i^k - I_{C_i}^k) = V_s - \sum_{j=1}^n Z_{ij} I_j^k, \quad i=1, \dots, n \quad (10)$$

where Z_{ij} is the common impedance between bus i and j . It is the summation of line impedances through them current injections of bus i and j flow together. The formation and characteristics of this Z matrix are the same as that given in [26]. Equation (10) after some manipulation can be expressed in matrix form for all buses as

$$([Z] + [Z_L^k]) [I^k] = [V_s] + [Z_L^k] [I_C^k] \quad (11)$$

where Z_L is a $n \times n$ square diagonal matrix and its main diagonal elements contain the equivalent load impedances. It is clear that the equivalent load impedances are much greater than the line impedances. Therefore, it can be written as

$$[Z][I^k] \ll [Z_L^k][I^k] \quad (12)$$

Using (12) and this fact that the difference of current injections at two consecutive iterations is too small, an accurate approximation can be obtained as

$$([Z] + [Z_L^k]) [I^k] \cong [Z][I^{k-1}] + [Z_L^k][I^k] \quad (13)$$

This approximation has no effect on the accuracy of the proposed method as clearly explained in simulation results. Now current injections can be obtained from (11) and (13) as

$$[I^k] = [Z_L^k]^{-1} \left([V_s] + [Z_L^k][I_C^k] - [Z][I^{k-1}] \right) \quad (14)$$

$$[Z_L^k]^{-1} = [Y_L^k] \quad (15)$$

where Y_L is a $n \times n$ square diagonal matrix and its main diagonal elements represent equivalent load admittances. It is clear that such admittances are the reverse of equivalent load impedances. This simplicity of inversion is the direct result of this fact that Z_L is a square diagonal matrix; therefore, Y_L can be calculated by reversing the main diagonal elements of Z_L . As can be seen in (14) and (15), the matrix inverting operation is no longer required in the proposed method which makes the method performance more superior to others. In fact, the main reason of divergence, time-consuming and heavy computations in power flow algorithms is related to the calculation of matrix inverse. Therefore, this elimination can be considered as a great contribution in DS analyses.

Up to now, the proposed DM has been presented. The outline of the algorithm is to solve (16) through (19) iteratively,

$$Z_{P_i}^k = |V_i^k|^2 / S_{P_i}^* \quad i=1,2,\dots,n \quad (16)$$

$$I_{C_i}^k = |I_{C_i}| \angle (\delta_i^k - \theta_i) \quad i=1,2,\dots,n \quad (17)$$

$$[I^k] = [Y_L^k]([V_s] + [Z_L^k][I_C^k] - [Z][I^{k-1}]) \quad (18)$$

$$[V^{k+1}] = [V_s] - [Z][I^k] \quad (19)$$

It can be seen that only the equivalent load impedances of constant power loads and the phase of the current of constant current loads are variable and must be updated at each iteration. The proposed method can easily be expanded to solve the three-phase power flow problem where the line impedances and current injections must be presented with 3×3 matrices and 3×1 vectors, respectively.

3- Algorithm Development for WMDSs

Normally closing open tie-switches creates a few meshes in DSs and such switches introduces WMDSs; however, the number of such meshes is restricted and WMDSs have been discussed often with one mesh in the literature. In this section, a new method for solving DSs with one mesh is proposed. The proposed method is applied to the meshes independently from their locations and is as efficient and simple as RDSs. Also, it can deal with more than one mesh in DSs.

To solve WMDSs as simple as RDSs, the solution of RDSs is applied to WMDSs. In radial feeders, current injection paths are completely defined but when a mesh is created, some of the current injections can flow through two paths as shown in Figure 3, e.g. I_k can flow through *Path1* and *Path2*. In fact, to apply RDS solution, in this case, the proportions of each current injection in the two paths must be specified. If these proportions are specified, considering the electrical circuit theory, the voltage drops of different paths, through which a bus is fed, are the same. Consequently, to calculate the voltage of each bus, one of such paths is selected and swept. In order to calculate current proportions, assume that current injections of the buses located on the mesh flow completely through their paths (e.g. current injections of d and e flow completely through *Path1* and current injections of f and g flow completely through *Path2*). It should be noted that the end of meshes can change in such a way that this assumption would always be correct. Also, assume that the current proportions of the buses located after the mesh flowing through *Path1* and *Path2* are α_1 and α_2 , respectively (e.g. $\alpha_1(I_h + I_k + \dots + I_l)$ flow through *Path1* and $\alpha_2(I_h + I_k + \dots + I_l)$ flow through *Path2*). Now the remaining problem is to calculate α_1 or α_2 . Three methods are suggested for this purpose and are presented in the following.

3- 1- Current divider rule

If current injections of the buses located on the mesh can be neglected, then the current divider rule can be used to calculate α_1 (α_2), e.g. in Figure 3, it can be calculated as

$$\alpha_1 = z_{Path2} / (z_{Path1} + z_{Path2}) = 1 - \alpha_2 \quad (20)$$

where z_{Path1} and z_{Path2} are the impedances of *Path1* and *Path2*.

3- 2- KVL method

The Kirchhoff's Voltage Law (KVL) can be applied to the mesh to calculate α_1 (α_2). The nominal values of the current

injections are used to solve the KVL equation. This method is also approximate; however, it is more accurate than the current divider rule.

3- 3- Auxiliary equation method

In order to accurately solve the above KVL equation, the calculated amount of current injections must be used at each iteration. Hence, the KVL equation must be solved using the updated amount of current injections after each iteration. In other words, α_1 (α_2) is updated at each iteration which makes the accuracy high but an auxiliary equation is needed.

To solve WMDSs as efficiently and simply as RDSs, the solution procedure of WMDSs is kept the same as that of RDSs and the changes are applied to Z matrix. The proposed method calculates the voltage drops of paths as $\Delta V = z(\alpha_i)$. To apply changes in the construction of the common impedances, voltage drops can be rewritten as $\Delta V = (\alpha_i z) i$. This technique is very useful for computer simulations.

The calculations of some elements of the Z matrix are partly different for WMDSs when α_1 or α_2 are applied to this matrix. Let bus i be closer to the substation bus S than bus j . To calculate Z_{ij} , as can be seen in Figure 3, there are three conditions as it follows. Please note that in the equations ahead, z_{xy} is the impedance of a line(s) connecting bus x to bus y . *Path1* is swept to calculate voltage drops of the buses located after the mesh.

A) If bus i is located before the mesh, the calculation process will be the same as that in RDSs, i.e.

$$Z_{ij} = Z_{ji} = z_{Sa} |_{i=a} \text{ OR } z_{Sb} |_{i=b} \text{ OR } z_{Sc} |_{i=c} \quad (21)$$

B) If bus i is located on the mesh, there are two conditions as:

a) bus j is also located on the mesh.
i. If two buses are located on the same path e.g. *Path1* ($i = d, j = e$), their current injections flow completely through the line between c and d , thus,

$$Z_{ij} = Z_{ji} = z_{Sc} + z_{cd} \quad (22)$$

ii. If two loads are located on different paths ($i = d$ & $j = f$), there is no common impedance between current injections on the mesh, thus,

$$Z_{ij} = Z_{ji} = z_{Sc} \quad (23)$$

b) If bus j is located after the mesh, there are two conditions as:

i. If bus i is located on the selected path, *Path1*, (e.g. $i = d$) I_i flows completely through the line between c and d , but the proportion of I_j flowing through this line is α_1 , i.e. current proportions of bus i and j flowing through the line between c and d are 1 and α_1 , respectively, then,

$$Z_{ij} = z_{Sc} + \alpha_1 z_{cd} \quad (24)$$

$$Z_{ji} = z_{Sc} + z_{cd} \quad (25)$$

ii. If bus i is located on the other path, *Path2*, (e.g. $i = f$), I_i does not flow through the line between c and d but the current proportion of bus j flowing through the line between c and f is α_2 , then,

$$Z_{ij} = z_{Sc} + \alpha_2 z_{cf} \quad (26)$$

$$Z_{ji} = z_{Sc} \cdot Z_{ji} = Z_{bi} Z_{ji} = Z_{bi} \quad (27)$$

C) If both buses are located after the mesh ($i = k, j = l$), their

current proportions flowing through *Path1* are α_p , then,

$$Z_{ij} = Z_{ji} = z_{Sc} + \alpha_1 z_{Path1} + z_{hk} \quad (28)$$

It can be seen that the changes were easily applied to the *Z* matrix and this matrix lost its symmetry for WMDSs. There is no need to change the solution process to analyze WMDSs and, moreover, the method is as efficient and simple as that of RDSs. As can be seen, the proposed method uses no additional features which may decrease its efficiency to analyze meshes.

4- Distributed Generation Modeling

In recent years, distributed generations (DGs) become more popular and the total installed capacity of such sources has grown drastically. These are usually used in constant power factor mode due to the fact that the standards prohibit active voltage regulation [33]-[34]. In this mode, when the active powers of them are specified, DGs can be modeled as *PQ*-buses.

It is clear when a DG is connected to the network, it sends current in reverse direction regarding the loads. Therefore, the only modification to study DGs in the proposed method is to consider their power injections with minus sign.

5- Simulation Results

In this section, first the three main categories of power flow algorithms are compared and then the accuracy of the proposed method for WMDSs is verified. In the end, a network with two meshes and a DG is analyzed to show the ability of the method. The proposed method was implemented within the MATLAB computing environment using Intel® Pentium® Dual-Core Desktop Processor E5200, 2.5 GHz and 2GB memory. The convergence tolerance is set at 0.0001 p.u.

5- 1- Comparison of the three main methods

As mentioned in Introduction, several methods based on the three main categories have been presented to solve DSs. In [13] and [25], it was shown that the performance of the CIMs is better than that of other NR methods and BFSMs. The BFSM suggested by [24] is also selected as the representative of BFSMs. Among DMs, the method suggested by [27] has a better performance than the others' and must be studied.

Case I: 90-Bus with Extreme Radial Topology: First the 90-bus test system with extreme radial topology shown in Figure 4 and taken from [24] is studied. Important computational parameters which must be compared are Number of Iterations (NIs) and Normalized Execution Time (NET). Table 1 gives the results of such parameters for this practical feeder.

It should be noted that the results of the BFSMs are taken from [24]. The PC used in this paper is not faster than that in [24] (the processors used in this paper and [24] are 2.5 GHz and 2.6 GHz, respectively, and the PC memories are the same). Table 1 shows that the performance of DMs is better than that of the other methods. There is no considerable difference between NIs but the two DMs are almost four times faster than the others. On the other hand, when the initial values of the *R/X* ratios are increased to verify the robustness of the methods, the proposed DM converged until $37 \times (R/X)$ even though the voltage of bus 7 was as low as 0.4718 p.u. However, this factor is equal to 28 for the CIM, and it is not specified for BFSM by [24]. Therefore, in addition to the simplicity and time-saving, the robustness is another advantage of the DMs. The two DMs have almost the same performances. The DM

presented by [27] only calculates the voltage drop of each bus voltage, and the load characteristics are not considered. Moreover, only a simple method for the constant power loads is presented. However, in this paper, the load voltages are converted to the equivalent load impedances which simplify the simulating of different models of loads. In fact, one of the main advantages of the proposed method is to simplify the method suggested by [27] to simulate different load models while its performance is not affected.

Case II: 11-Bus With Two DG Simple Radial Feeder: To study a simple radial feeder including DGs, a simple feeder shown in Figure 5 is studied. DGs have not modeled in [16], [27] & [24]. To verify the robustness of the proposed method, the initial values of the loads and *R/X* ratios of this feeder were gradually increased up to the point where convergence was no longer attained. The NIs required to find the solution when all loads were varied by factor β is shown in Figure 6. Figure 7 shows the performance of the methods when the initial *R/X* ratios of the branches were increased by factor γ . It can be seen that the proposed method is numerically robust and requires fewer iterations than the CIM. In Figure 6, when each load is increased up to 9.4 times, convergence is still attained in the proposed method. This value is equal to 6.6 in the CIM.

Case III: Three-Phase Study: IEEE 37 Node Test Feeder: To study three-phase power flow, the IEEE 37 Node Test Feeder shown in Figure 8 is studied. This feeder is highly unbalanced and has three models of loads. Its loads are connected in the delta but in this study, they are considered line-to-ground. Moreover, regulator and the transformer connecting node 709 to 775 are not implemented in this paper. In order to verify the robustness of the proposed method as *case II*, the same process is applicable in this study. Figures 9 and 10 show the effect of different system loadings and *R/X* ratios on NIs. The same conclusions can be derived in this case where studies show the robustness and fast convergence of the proposed DM.

Another advantage of DMs is low computations, e.g. in *case I*, only 89 different elements must be calculated to construct its *Z* matrix and to implement the proposed DM. The 90-bus system includes only 45 loads, therefore, only 45 equivalent load impedances are necessary to be calculated. However, a 90×90 Radial Configuration Matrix (RCM) which has 179 nonzero terms must be constructed in the BFSM suggested by [24]. Then, this RCM must be inverted to construct a Section Bus Matrix (BSM) and, moreover, the BSM must be transported to construct another required matrix (Bus Section Matrix). In CIM proposed in [13], a 178×178 Jacobian matrix must be constructed and the inverse of this matrix must be calculated at each iteration. On the other hand, the number of Jacobian matrix elements which must be updated at each iteration is equal to 4×45 for the CIM while it is only 45 for the proposed method.

5- 2- WMDS Study

Different studies on the accuracy of the proposed method for WMDSs show that the worst case occurs when there is a high load imbalance on the two paths of the mesh. The feeder shown in Figure 11 is studied to simulate such conditions where loads are centralized at three points and total loads

are constant. That portion of the load located on *Path 2* is increased by the factor β while there is no load on *Path 1*. In other words, the percentage of the load imbalance on the two paths is increased. Figure 12 shows maximum errors of the proposed method when the three methods are used to calculate α_1 (α_2).

It should be noted that the maximum errors of the KVL and auxiliary equation methods are multiplied by 100 in Figure 12. As can be seen in this figure, the maximum errors of these two methods are much smaller than the convergence tolerance in all conditions. The maximum errors in the current divider rule method are also smaller than the convergence tolerance upto 15% load imbalance and it is restricted to 0.007 p.u. in complete load imbalance (when all loads are located on *Path 2*). Moreover, the feeder loadings in Figure 11 are heavy, thus, the voltage of bus *c* drops to 0.96 p.u. Hence, the current divider rule method can be used for normal WMDSs when the load imbalance on the two paths is less than 15% of total loads. It is clear that most of the load imbalances of WMDSs are smaller than 15%. In severe cases, the KVL method can be used which always has a reasonable accuracy. Therefore, as shown in Figure 12, WMDSs are solved using only the current divider rule or the KVL for the mesh. There is no need to use the auxiliary equation method at all.

5- 3- Highly meshed DSs with penetration of DG

To show that the proposed method can solve the systems with more than one mesh and with DG, the system shown in Figure 13 where all line impedances are equal to $0.0125 + 0.00625i$ p.u. is analyzed employing the KVL method to solve the meshes. It is difficult to find such systems in DSs where the system has two meshes and one DG. Table 2 shows the bus voltages where the maximum error is 0.0004 p.u. Hitherto, we presented the proposed method, and the results showed its advantages. Simplicity, robustness, low computations, and fast convergence are the main advantages of the proposed method. The proposed direct power flow method takes three steps forward in comparison with the other DMs as it uses no matrix inversion, simulates different models of loads, and solves WMDSs as efficient and simple as RDSs.

6- Conclusion

In this paper, a new DM for DS power flow analysis was developed. The proposed DM uses neither matrix inversion nor its related computations. It considers load characteristics by defining the equivalent load impedances which simplifies the simulating of different models of loads. It was shown that the proposed method is numerically very robust and requires fewer iterations. Moreover, the proposed DM has been developed for WMDSs using three simple methods for solving meshes. The performance of the proposed approach for WMDSs is as efficient and simple as RDSs. The results show that the present method is efficient and suitable for large-scale DSs.

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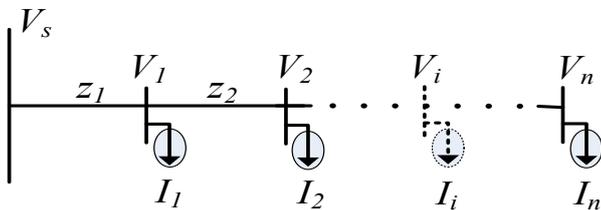


Figure 1: Single line diagram of a simple main distribution feeder

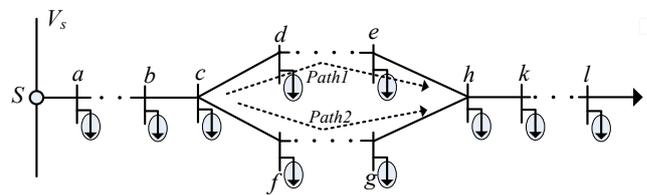


Figure 3: One line diagram of a central mesh

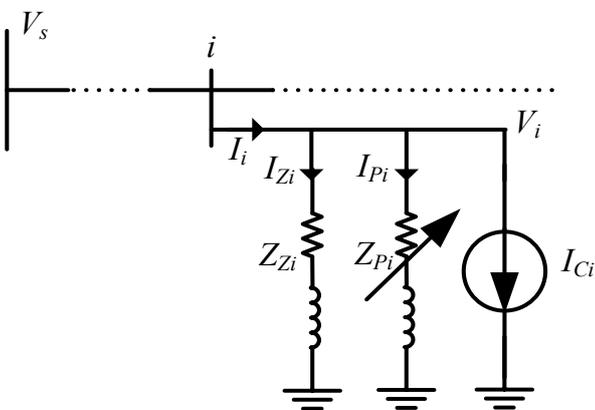


Figure 2: A bus with three models of loads or a combination load

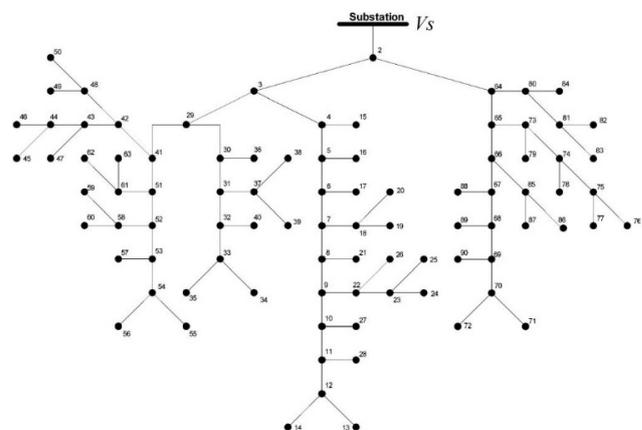


Figure 4: A 90-bus radial distribution feeder

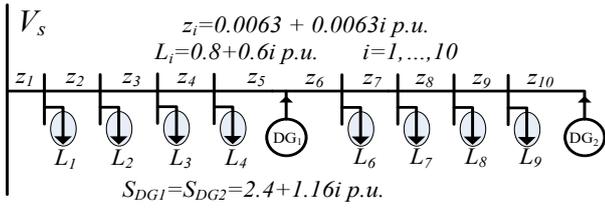


Figure 5: A simple 11-bus feeder with 2 DGs

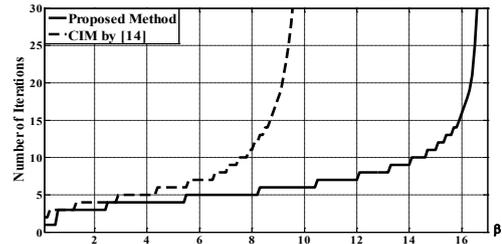


Figure 9: Comparative diagram for different system loadings of the IEEE 37 Node Test Feeder

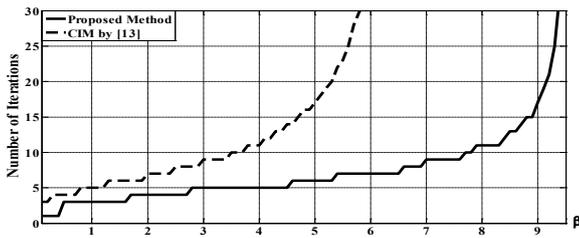


Figure 6: Comparative diagram for different system loadings of the feeder shown in Figure 5.

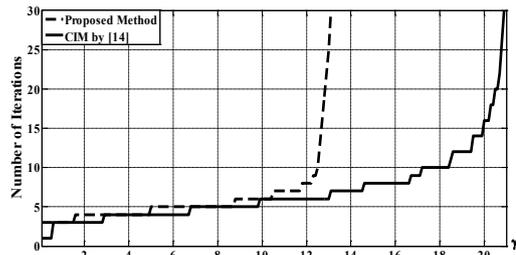


Figure 10: Comparative diagram for different R/X ratios of IEEE 37 Node Test Feeder.

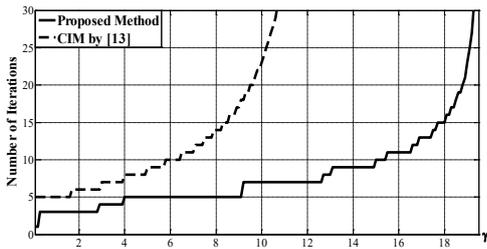


Figure 7: Comparative diagram for different R/X ratios of the feeder shown in Figure 5.

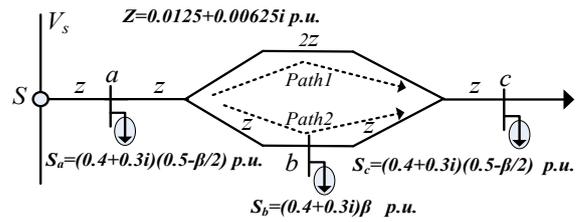


Figure 11: A feeder with one mesh (the worst case study)

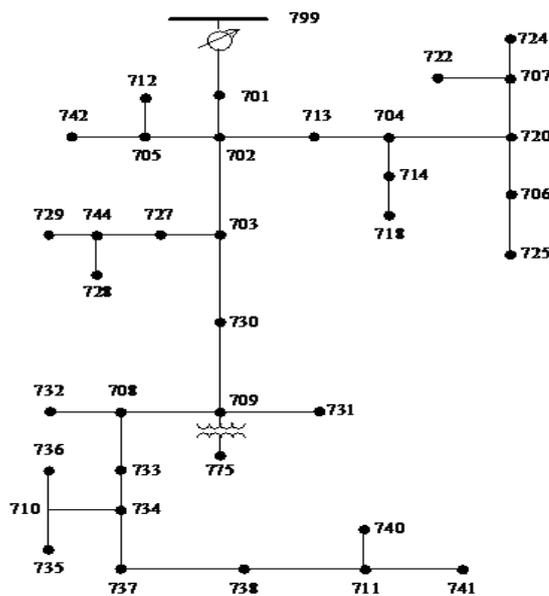


Figure 8: IEEE 37 Node Test Feeder

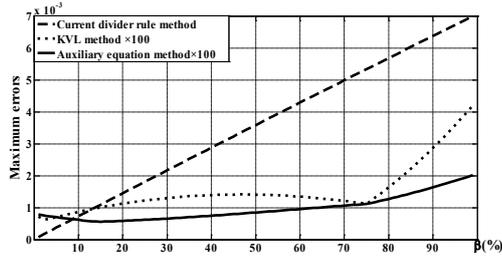


Figure 12: Maximum errors of the proposed method when the load imbalance on the two paths shown in Figure 11 is increased by factor β (%)

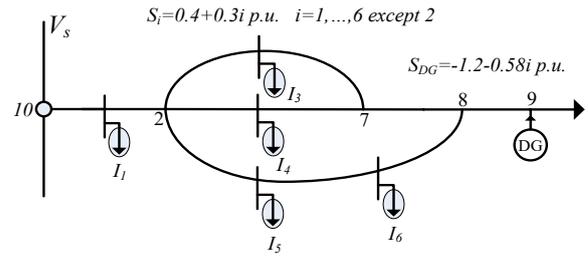


Figure 13: A system with two meshes and a DG

Tables

Table 1: Results of computations for Case 1

Methods	NIs			NET		
	Original R/X	15 R/X	37 R/X	Original R/X	15 R/X	37 R/X
Proposed Method	3	5	92	1.00	1.09	4.12
DM by [27]	3	5	92	1.12	1.18	4.24
CIM by [13]	3	6	Diverged	8.18	11.21	Diverged
BFSM by [24]	3	5	NA	3.33	3.85	NA
BFSM by [16]	4	6	NA	3.93	4.58	NA

Table 2: Bus voltages of the system shown in Figure 13

Bus No.	The proposed method	CIM [13]
10	1.0000	1.0000
1	0.9863	0.9863
2	0.9787	0.9787
3	0.9765	0.9764
4	0.9765	0.9764
5	0.9754	0.9756
6	0.9783	0.9787
7	0.9804	0.9802
8	0.9881	0.9878
9	1.0049	1.0045

NA: Not Available

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